

ECE 6214: Optoelectronic Devices
VIRGINIA TECH
Course Syllabus (CRN 89138)
Fall 2018 MW 4:00-5:15 PM

I. ECE 6214 OPTOELECTRONIC DEVICES

Instructor: Prof. Mantu Hudait, Dept. of ECE, 626 Whittemore Hall
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Class Room: **RAND 206A**

Final Exam date and time	Exam Date: December 7th, 2018	Midterm-I (in class)	September 19th
	Begin Time: 1:05 PM	Midterm-II (in class)	October 24th
	End Time: 3:05 PM	Final (in class room)	December 7th, 2018

Office Hours:

Wednesday: 10:00 am-12:00 pm and by appointment (e-mail please).

2. Course Description:

Optoelectronics and integrated optics are needed to transfer electrical current into optical signals and vice versa and are used in applications that include communications, sensing, bio-medical instrumentation, consumer electronics, and defense. Understanding the underlying physics and technology of the modern optoelectronics and integrated photonics are important for the above applications. This course will provide a graduate level understanding of the principles of light generation and detection, operation, and design of the state-of-the art of optoelectronic and photonic devices. Topics include advanced treatment of the operating principles of semiconductor optoelectronic devices with direct comparison to experimental data reported in the literature.

3. Learning Objectives

Upon successful completion of this course, students should be able to:

- (1) Explain key concepts in quantum and statistical mechanics relevant to physical, electrical and optoelectronic properties of materials and their applications to optoelectronic devices and photonic integrated circuits that emit, modulate, switch, and detect photons.
- (2) Describe fundamental and applied aspects of optoelectronic device physics and its applications.

The lecture session provides learning opportunities that should enable students to do the following upon completion of this course:

A. Develop a basic understanding on the following key concepts in quantum and statistical mechanics relevant to physical, electrical and optoelectronic properties of materials and their applications to optoelectronic devices and photonic integrated circuits that emit, modulate, switch, and detect photons.

i. Quantum mechanics:

Crystal structure of solids; propagating electron wave in a periodic lattice; elemental and compound semiconductor materials, alloy semiconductors, reciprocal lattice.

ii. Electronic structure of semiconductor heterostructures:

Energy bands, density of states in 3D, 2D, and 1D; non-degenerate and degenerate semiconductors; heavy doping effect; carrier scattering phenomena, density of carries in intrinsic and extrinsic semiconductors, band tail states.

iii. Optical processes in semiconductor heterostructures:

Maxwell equations and boundary equations; recombination, absorption in semiconductors, matrix elements and oscillator strength for band-to-band transitions, absorption and emission spectra, Auger recombination.

iv. Heterojunction and quantum wells:

Theory of the heterostructure band alignment; transport properties; drift-diffusion equation; generation-recombination; abrupt structures and thermionic emission.

v. Propagation of light:

Light propagation in isotropic and anisotropic media; optical waveguide theory; dielectric optical waveguides; the effective index method, gain guidance and index guidance in semiconductor laser; losses and gains in waveguide.

vi. Generation of light:

Semiconductor lasers; Fabry-Perot and distributed-feedback lasers and vertical-cavity surface-emitting lasers (VCSEL).

vii. Detection of light:

Solid state detectors

viii. Solar cells (If time permits):

Carrier recombination and lifetime; carrier transport of p-n junction under illumination; solar cell parameters and device design; III-V heterojunctions single and multijunction solar cells; tailor-made bandgaps for matching solar spectrum.

B. Become proficient with the fundamental and applied optoelectronic device physics and its applications.

C. Learn to analyze optoelectronic device characteristics in detail and brainstorm ways towards improving them or adapting them to new applications.

4. Text Book: **Jia-Ming Liu** book will be the main text book for this course, however, different book chapters will be used as the major source of information and it will be posted in Canvas.

Lasers and Detectors:

- Jia-Ming Liu, *Photonic Devices*, Cambridge University Press, New York 2005; ISBN 978-0-521-13578-8
- Shun Lien Chuang; *Physics of Photonic Devices (2nd Edition)*; John Wiley & Sons (2009)
ISBN-10: 0470293195; ISBN: 978-0470293195 (paperback)

Heterostructures:

- Manasreh, *Semiconductor Heterojunctions and Nanostructures*, McGraw Hill

Solar cells:

- Nelson, *The Physics of Solar Cells*, Imperial College Press

Link:

- <http://ecee.colorado.edu/~bart/book/>

- http://cleanroom.byu.edu/EW_orientation.phtml
- <http://www.ioffe.rssi.ru/SVA/NSM/Semicond/index.html>

5. Pre-requisite: ECE 5200 or ECE4214

6. Detailed Syllabus

I.	Introduction to course	
II.	Quantum mechanics	5%
	a. Crystal structures	
	b. Particle in a box	
	c. Bandgap	
III.	Electronic structure of semiconductor heterostructures	10%
	a. Density-of-states: 3D, 2D, 1D	
	b. Effective mass	
	c. Heavy doping effect	
IV.	Optical process in semiconductors	5%
V.	Heterojunction band alignment and quantum wells	10%
	a. Theory of band alignment	
	b. Measurement and interpretation of band alignment	
	c. Drift-diffusion	
	d. Transport properties	
	e. Heterojunction diode current	
VI.	<i>Propagation of light</i>	5%
VII.	<i>Semiconductor lasers</i>	20%
	a. Quantum well lasers	
	b. Vertical cavity surface emitting lasers	
VIII.	<i>Photodetectors</i>	20%
	a. Quantum efficiency; gain and bandwidth;	
	b. Integrated laser-modulator; multi-section phase; gain;	
	c. Distributed Bragg reflector devices	
IX.	<i>Solar cells</i>	25%
	a. Carrier recombination and lifetime	
	b. Carrier transport under illumination	
	c. Heterojunction III-V single and multijunction cells	
	d. Design of a multijunction solar cell	

7. Grading Policy

Homework	20%
Midterm-I and II	45% (20%+25%)
Final Exam	30%
Class participation	5%
	100%

Home Work:

Homework problems will be typically be assigned throughout the semester and will be due at the end of class one week following its assignment. Late homework will be accepted only in extraordinary circumstances AND by specific arrangement with the instructor. It is your responsibility to present your work in a clear and logical fashion. No assignments will be accepted beyond one day late, except in the case of unforeseen, officially documented absences. *Each* problem solution should be neatly worked out. You may consult with other students and with your instructor while you are working on assigned problems but your goal in consulting should be limited to exploring options and approaches rather than avoiding work. Experience tells us if you do not work on the homework yourself, you will pay for it later during exams. The ability to solve problems develops through disciplined effort and the exams will require you to be able to solve problems. To obtain full credit for a homework assignment you must submit it to your instructor in class on the due date. Experience tells us if you do not work on the homework yourself, you will pay for it later during exams.

Computer problems: Some of the homework may involve computer problems. It is expected students are familiar with one or more basic programming languages to solve numerical problems (C, Basic, FORTRAN, MATLAB, Mathematica etc.) Students may need to familiarize themselves with device simulators like SenTaurus, BandProf, NextNano etc.

In-Class Activities:

Students have the ultimate responsibility for their learning and must decide for themselves on how to maximize progress and efficiency. Class attendance, participation in classroom discussions, reading the text in advance of class lectures, homework effort and the use of office hours may all be collectively viewed by the instructor as indications of student's interest and effort in learning the course material in addition to exams.

Exams:

There will be **two mid-term exam and a final exam**. Two sheets of course notes may be used during the exam and other materials including textbook is not allowed.

Academic Integrity:

The Virginia Tech Honor Code establishes the standard for **ACADEMIC INTEGRITY** in this course, and will be strictly enforced. *Discussion* of class material with your classmates or the instructor is encouraged; however, ALL submitted work, must represent your own efforts, and you must pledge to this effect on all work. For more details on the relevant honor codes, consult the websites listed below:

- [Graduate Honor System, http://filebox.vt.edu/studentinfo/gradhonor/](http://filebox.vt.edu/studentinfo/gradhonor/)

Students enrolled in this course are responsible for abiding by the Honor Code. A student who has doubts about how the Honor Code applies to any assignment is responsible for obtaining specific guidance from the course instructor before submitting the assignment for evaluation. Ignorance of the rules does not exclude any member of the University community from the requirements and expectations of the Honor Code. For additional information about the Honor Code, please visit:

- <https://www.honorsystem.vt.edu/>
- All assignments submitted shall be considered "graded work" and all aspects of your coursework are covered by the Honor Code. All projects and homework assignments are to be completed individually unless otherwise specified.
- Commission of any of the following acts shall constitute academic misconduct. This listing is not, however, exclusive of other acts that may reasonably be said to constitute academic misconduct. Clarification is provided for each definition with some examples of prohibited behaviors in the Undergraduate Honor Code Manual located at <https://www.honorsystem.vt.edu/>

A. CHEATING

- Cheating includes the intentional use of unauthorized materials, information, notes, study aids or other devices or materials in any academic exercise, or attempts thereof.

B. PLAGIARISM

- Plagiarism includes the copying of the language, structure, programming, computer code, ideas, and/or thoughts of another and passing off the same as one's own original work, or attempts thereof.

C. FALSIFICATION

- Falsification includes the statement of any untruth, either verbally or in writing, with respect to any element of one's academic work, or attempts thereof.

D. FABRICATION

- Fabrication includes making up data and results, and recording or reporting them, or submitting fabricated documents, or attempts thereof.

E. MULTIPLE SUBMISSION

- Multiple submission involves the submission for credit—without authorization of the instructor receiving the work—of substantial portions of any work (including oral reports) previously submitted for credit at any academic institution, or attempts thereof.

F. COMPLICITY

- Complicity includes intentionally helping another to engage in an act of academic misconduct, or attempts thereof.

G. VIOLATION OF UNIVERSITY, COLLEGE, DEPARTMENTAL, PROGRAM, COURSE, OR FACULTY RULES

- The violation of any University, College, Departmental, Program, Course, or Faculty Rules relating to academic matters that may lead to an unfair academic advantage by the student violating the rule(s).

Academic Misconduct Sanctions:

“If you have questions or are unclear about what constitutes academic misconduct on an assignment, please speak with me. I take the Honor Code very seriously in this course. The normal sanction I will recommend for a violation of the Honor Code is an **F*** sanction as your final course grade. The F represents failure in the course. The “*” is intended to identify a student who has failed to uphold the values of academic integrity at Virginia Tech. A student who receives a sanction of **F*** as their final course grade shall have it documented on their transcript with the notation “FAILURE DUE TO ACADEMIC HONOR CODE VIOLATION.” You would be required to complete an education program administered by the Honor System in order to have the “*” and notation “FAILURE DUE TO ACADEMIC HONOR CODE VIOLATION” removed from your transcript. The “F” however would be permanently on your transcript.”

Announcements:

I will use Canvas to post lecture notes, homework assignments, homework solutions, and other information pertaining to the course materials. You should check your email and the Canvas on a regular basis.

Attendance:

Attendance all lecture classes is expected and critical to your successfully completing the requirements of this course. Though it is not a policy, come to the class and have fun! Since it is an advanced level class, it will greatly help you while you are present in class. In the event that you miss a lecture, it is your responsibility to obtain the missed notes from one of your classmates or from Scholar. If you have a conflict with a scheduled exam or with the submission of any in-class assignments, you must make arrangements with your instructor well in advance so that alternate times can be scheduled.